

INFORMATION DISCLOSURE CITATION	Attorney Docket No. 10205.028	Group
	Applicant Story et al.	Serial No.
		Filing Date January 25, 2001

PTO  
U.S.  
09/769564  
01/25/01

## U.S. PATENT DOCUMENTS

Init.	Document Number	Date	Name	Class	Sub-class	Filing Date
Rs	4748663	5-88	Phillips et al.	379	388	
Rs	5386465	1-95	Addeo et al.	379	202	
Rs	5455543	10-95	Kechkaylo	332	103	
Rs	5455544	10-95	Kechkaylo	332	103	

## FOREIGN PATENT DOCUMENTS

Document Number	Date	Name	Class	Sub-class	Translation

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Rs				Brennan, Paul V., <i>Phase-Locked Loops: Principles and Practice</i> , R.R. Donnelley & Sons Company (1996), pages 8-9

EXAMINER

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